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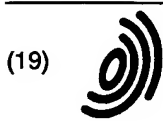
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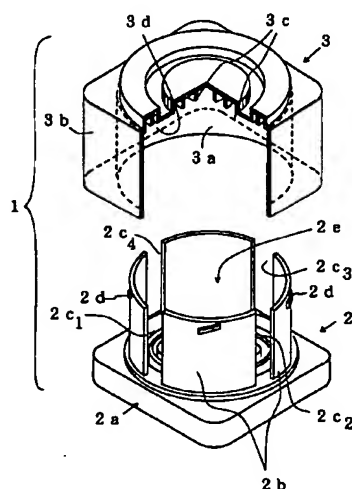
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(54) CONTAINER FOR HOUSING SEMICONDUCTOR WAFERS, STRUCTURE FOR HOUSING SEMICONDUCTOR WAFERS, AND METHOD OF PUTTING IN AND TAKING OUT SEMICONDUCTOR WAFERS

(57) There is provided an automatically controllable or a robot-oriented container exclusively used for containing wafers. There is also provided a wafer containing structure to prevent damage or breakage of semiconductor wafer due to impact and to prevent static electricity generated thereon due to friction. The present semiconductor wafer container comprises a container main body provided with a cylindrical portion in which a plurality of semiconductor wafers and spacer sheets are stacked and contained, and a lid body to be put on the container main body. In the present semiconductor structure of semiconductor wafers in which a plurality of semiconductor wafers are contained in a container main body through spacer sheets.

Fig.1



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Description

TECHNICAL FIELD

This invention relates to a disk-like semiconductor wafer container, a containing structure and a method for putting semiconductor wafers in and/or out of the container, and more particularly relates to a semiconductor wafer container, a containing structure and a method for putting wafers in and/or out of the container to prevent impact breakage of the semiconductor wafers during transportation and static electricity caused by friction thereof.

BACKGROUND OF THE INVENTION

In general, semiconductor wafers (hereinafter simply referred to as wafers) are prepared by cutting a silicon monocrystal ingot into thin disks of, for example, about two to eight inches in diameter. The wafers are allowed to apply many circuits such as LSI and cut into chips, which are then packaged to form a semiconductor device.

Each process for cutting such an ingot into wafers, forming circuits on the wafer surface and cutting the wafers into chips to package is often conducted independently at different sites. Generally, a container is used to transfer the wafers from one site of a specific step to the other site. Further, the wafers should be carefully put in the container because electrostatic damage or effect to the wafer surface where circuits are formed would decrease the yield.

It is conventionally known to form many grooves on an inner wall surface of the container so as to put the wafers between such grooves. It is also known to stack each wafer in order through polyethylene films in the container so as to form a containing structure.

In the case of the above mentioned first containing structure where many grooves are formed in the inner wall surface, however, mechanical vibration during transportation or accidental drop impact is transmitted to the wafers through the container, thereby the wafers being physically or mechanically damaged. While, in the case of the second containing structure where each wafer is stacked through polyethylene films, the wafers and the films tend to move slightly each other due to mechanical vibration during transportation. Static electricity is generated by such a slight movement and charged on the polyethylene films and the wafers, which would damage circuits on the wafers when the electricity is discharged.

Further, when the wafers are put in and out of the container by hand, the wafers would be damaged due to, for example, malfunction. Accordingly, it is preferable to use an automatic control mechanism to put the wafer in and out, but an introduction thereof to conventional containers makes the system more complicated.

OBJECT OF THE INVENTION

It is an object of the present invention to provide an automatically controllable or a robot-oriented container exclusively used for containing wafers.

Another object of the present invention is to provide a wafer containing structure in which the above mentioned conventional problems are solved so as to prevent a physical and electrical damage of wafers caused by impact during transportation.

A further object of the present invention is to provide a wafer containing structure which can avoid an inconvenient situation where wafers and spacer sheets are united when they are put out of a container because of close and tight contact.

A still further object of the present invention is to provide a method for putting wafers in and/or out of an automatically controllable container for exclusively containing wafers.

SUMMARY OF THE INVENTION

A wafer container of the present invention characteristically comprises a container main body having a cylindrical portion in which a plurality of wafers are stacked through spacer sheets and a lid body to be put on the container body. The cylindrical portion may have at least a slit to receive a pickup arm for easily putting the wafers in and/or out of the container. Further, the container main body has the cylindrical portion standing on an ichnographically square base portion, and the lid body is ichnographically square form which is practically similar to ichnographical form of the base portion of the container main body, the lid body and the container main body being fitted by relatively turning each other to ichnographically conform both bodies.

In order to immediately flow out static electricity generated in the container or to shut out an effect of external electric field against inside of the container, surface resistivity thereof is preferably $10^6 \Omega/\square$ or less. Typically, the container may be formed by monolithically molding a conductive filler-added conductive plastic material or a polymer alloy-treated conductive plastic material. The conductive filler to be added includes carbon black, graphite carbon, carbon fiber, metal powder, metal fiber, powdery metal oxide, metal-coated inorganic fine powder, organic fine powder and organic fiber.

The present invention provides a wafer containing structure in which a plurality of wafers are stacked and contained through spacer sheets in the container main body. There may be put end-cushioning materials on a top-end and under a bottom-end portions of the wafers stacked and contained in the container to protect the wafers from impact. The container main body of the wafer container of the present invention is preferably used as a main body of the wafer containing structure.

The spacer sheets are generally made of paper, synthetic paper, synthetic resin films, synthetic resin

foam sheets and the like. These spacer sheets may either be electrically conductive or non-conductive. When the spacer sheets are conductive, surface electrical resistance is preferably $10^6 \Omega/\square$ or less. There may be used polyolefin synthetic paper which contains dispersed conductive fiber such as conductive plastic-conjugated fiber of polypyrrole, polyaniline, carbon fiber and metal-coated fiber; or films or foam sheets of polyethylene, polypropylene, polyethylene phthalate added with conductive fillers, as mentioned above, or antistatic agents.

Although the spacer sheets may either be mono- or multi-layer structure of the above mentioned material such as synthetic paper, the mono-layer structure is preferable from a standpoint of handling and other properties.

Also, synthetic paper of polypyrrole-conjugated fiber is preferable from a standpoint of easy control of conductivity, durability and the like.

The spacer sheets preferably have air permeability of 1,800 sec/100 cc or less according to Japanese Industrial Standard (JIS) P8117, smoothness of 10 sec or less according to JIS P 8119 and dust repellency of 200 particles ($>0.5 \mu\text{m}$)/100 mm x 100mm or less according to the Japan CIC Company Standard.

In the container of the present invention, the container may be vacuum-packed as a whole for a purpose of dustproof when the wafers are contained in accordance with the containing structure. In general, as the surface of the wafers is extremely smooth, the spacer sheets and the wafers would adhere tightly and come into contact with each other under a practically airless condition.

Because of this, thus adhered wafers and spacer sheets are not easily separated when they are put out of the container in order and it is troublesome to strip off the wafers by hand from the spacer sheets one by one. Particularly, in the case of an automatic control system as will be described below, a situation where the wafers and the spacer sheets adhere tightly would result in problems when they are put in and out.

According to the structure of the present invention, it is possible to essentially avoid the above mentioned situation so as to solve conventional problems. Even if the wafers and the spacer sheets adhere each other, such a trouble can be easily removed by the structure of the present invention.

Each of the spacer sheets may be formed to provide a plurality of concave portions and/or convex portions on at least one surface, or to provide cut-lines extending from a periphery to inside thereof. The concave and/or convex portions may typically be formed by an embossing process. The cut-lines may typically be formed by a cutter or a clicker of certain width.

There may be used soft polyurethane foam, polyethylene foam, polypropylene foam, polystyrene foam and the like as the end-cushioning material. Closed-cell foam is preferable to shut out the dust from outside, while soft polyurethane is desirable from a standpoint of

cushioning properties. The cushioning material may be electrically conductive, surface resistivity thereof being preferably $10^{11} \Omega/\square$ or less. When the end-cushioning material is not conductive, it is preferable to arrange conductive spacer sheets between the uppermost wafer and the bottom one.

According to the containing structure of the present invention, each end-cushioning material is put on the top end portion and under the bottom end portion of the spacer sheets and the wafers stacked one after the other to contain them in the container.

It is preferable to use a cushioning material having 10 %-compressive stress of about 0.01 to 0.6 kg/cm². For example, polyurethane foam of about 0.01 to 0.03, polyethylene foam of about 0.2 to 0.4, and polystyrene foam of about 0.2 to 0.4 kg/cm² are preferably used.

In the above mentioned wafer container structure of the present invention, the wafers and the spacer sheets are stacked one after the other and contained in the container with the end-cushioning material put on the top and under the bottom ends. Accordingly, vibratory impact to the wafer container during transportation can be absorbed by the end-cushioning material, thereby protecting the wafers from such impact.

When a conductive material is used as the spacer sheets, no static electricity generates if friction is created between the wafers and the spacer sheets or between the wafers and the end-cushioning material. Even if the static electricity happens to generate, such electricity does not remain in the container. As a result, any circuit formed on the wafer never be inconveniently damaged by the static electricity.

Further, each wafer can be stably contained in the container without any mutual displacement thereof because the spacer sheets and the wafers are stacked one after the other and retained from both of the upper and the bottom directions in the container. Thus, contact breakage of the wafers to the inner surface of the container or static electricity caused by friction of mutual wafer can be controlled.

According to the present invention, there is provided a method for putting the wafers in and/or out of the container in which the wafers and the spacer sheets are put in and/or out of a cylindrical portion of a container main body (for example, the container main body used in the wafer container of the present invention), the cylindrical portion having at least one slit thereon, by means of one or two pickup arms. The present method is adaptable to an automatic controlling mechanism.

When one pickup arm is used, the arm enters into at least one slit and put each wafer and spacer sheet in the cylinder portion or put out therefrom.

When two pickup arms are used, one of the two arms enters into the slit, While the other one enters into the same or another slit. Each wafer and spacer sheet is put in or put out of the cylinder portion by means of these two pickup arms.

According to the present method for putting the wafers in and/or out of the container, the container pro-

vided with the silted cylinder is used, which makes a head portion of the pickup arm to enter into inner portion of the cylinder when the wafers are put in and/or out of the cylinder of a container main body (for example, the container main body used in the wafer container of the present invention). Accordingly, if numbers of the wafers increase gradually when they are put in, or if numbers thereof decrease gradually when they are put out, control of the pickup arm is quite easy in an automatic control mechanism.

BRIEF DESCRIPTION OF THE DRAWING

Fig. 1 is a partially cutaway perspective view of an example of a wafer container of the present invention;

Fig. 2 is a semi-vertical side view of a wafer container of Fig. 1;

Fig. 3 is a perspective view of an example of a containing structure of the present invention;

Fig. 4 is a schematic sectional side view of a container shown in Fig. 3 with wafers put therein;

Fig. 5 is a partially enlarged sectional view of a containing structure of the present invention with wafers contained therein;

Fig. 6 (A) is a schematic view of an embossed spacer sheet, which structure does not cause any adhesion with wafers, or breaks off such an adhesion if occurred;

Fig. 6(B) is a schematic view of a spacer sheet with a plurality of cut lines extending from outside to inside thereof, which structure does not cause any adhesion with wafers, or breaks off such an adhesion if occurred;

Fig. 7 is a schematic perspective view of a carrier system to which the present method for putting wafers in and/or out of the container can be introduced;

Fig. 8 is a schematic view showing a situation before wafers are put in a container by means of a carrier system shown in Fig. 7;

Fig. 9 is a schematic view showing a situation where wafers are being just put in a container by means of a carrier system shown in Fig. 7; and

Fig. 10 is a schematic view showing a situation after wafers have been put in a container by means of a carrier system shown in Fig. 7.

PREFERRED EMBODIMENTS

The present invention will be described in accordance of the drawing.

Fig. 1 is a partially cutaway perspective view of an example of a wafer container and Fig. 2 is a semi-vertical side view thereof. In Figs. 1 and 2, a wafer container 1 comprises a bottomed cylindrical container main body 2 for containing plurality of stacked disk-like wafers and a lid body 3 to be put on the container main body 2.

In the container main body 2, a cylindrical portion

2b is formed on a square base portion 2a to contain plurality of stacked wafers. An inner diameter of the cylindrical portion is nearly several mm larger than that of the wafers to be contained. The cylindrical portion 2b has slits 2c1 to 2c4 extending from the top to the bottom at circumferentially quadrisectioned positions on the base portion. Width of these slits 2c1 to 2c4 is wide enough to pass a pickup arm of a carrier robot therethrough as will be described in a putting in and/or out system. Each slit is preferably positioned in the center of each side of the base portion to shorten a stroke of the pickup arm. Each slit may also be positioned on diagonal lines of the base portion depending on a system to be used.

Four slant protrusions 2d are formed as stoppers at nearly equal spaces on the top of the outer surface of the cylindrical portion 2b, which are aligned at a inclined position with slant slots 3e formed inside of a circular channel 3d of the lid body 3. The container main body 2 or the lid body 3 is slightly turn round to fix both bodies. The base portion 2a of the container main body 2 has a concavo-convex upper surface (or an inner surface facing inside of the container) comprising a plurality of reinforcing concentric ribs of same height as shown in Fig. 2, where the wafers being put on horizontally.

On the other hand, the lid body 3 has an external shape of square pole as a whole and comprises a cylindrical portion 3a and a square pole portion 3b circumscribed to the portion 3a. The cylindrical portion 3a covers the cylindrical portion 2b of the container main body 2, while leaving a part of the base portion 2a of the main body 2 uncovered by a bottom portion of the cylindrical portion 3a. The cylindrical portion 3a plays a role as a guide means when the lid body is attached to or detached from the container main body 2.

On an inner top surface of the lid body 3, the circular channel 3d is formed to receive an upper end of the cylindrical portion 2b of the container main body 2, while a plurality of concentric ribs and convex portions (hereinafter simply referred to as ribs 3c) are formed as downward extrusions corresponding to a containing space 2e of the container main body 2. These ribs 3c are in contact with an upper portion of the stacked wafers in a situation where the lid body 3 is attached to the container main body 2 and the ribs 3c are slightly advanced into an upper portion of the containing space 2e. According to the present invention, however, the upper portion of the stacked wafers may simply be pressed downward by the inner surface of the lid body 3, and accordingly it is not necessarily essential to form the ribs 3c and advance into the containing space 2e.

The lid body 3 is detachable fitted to the container main body 2 by aligning the slant protrusions 2d with the slant slots 3e formed inside of the circular channel 3d. In more concrete terms, the lid body 3 is capped on the cylindrical portion 2b at a inclined position to the container main body 2 and advanced into the body 2 within an angle of the slant slots by turning round the lid body 3 at a predetermined angle (15° to 45°), thereby both bodies being gradually fixed each other. The under sur-

face of the container main body 2 is indented inside to receive the upper portion of the lid body 3, which allows the wafer container 1 to stack a number of wafers therein in the vertical direction.

A conductive polypropylene resin having volume resistivity of $200 \Omega \text{ cm}$, and surface resistivity of $10^6 \Omega/\square$ was used in the above embodiment.

An embodiment in which a plurality of wafers are contained in the wafer container 1 will be described in the following. Fig. 3 is a perspective view of the container main body 2 containing wafers therein. Fig. 4 is a vertical sectional view of the container main body 2 containing wafers. Fig. 5 is a partially enlarged sectional view of Fig. 2 showing wafers contained in the wafer container 1.

In these figures, a plurality of wafers 4 are stacked and contained in the containing space 2e of the container main body 2, while a spacer sheet 5 (a monolayer conductive sheet in Fig. 3) is put between each wafer 4.

Although a size of wafers generally varies, for example, a diameter ranging from 2 to 8 inches (about 50 mm to 200 mm) and thickness of 350, 500 and 750 μm depending on a purpose to be used, wafers of 350 μm in thickness and 8 inches in diameter was used in the present embodiment. The wafer container used in the present embodiment is designed to meet the above mentioned size and contain 25 wafers.

Spacer sheets 5 function to prevent direct contact and friction of mutual wafers 4 when they are stacked and to control generation of static electricity between the wafers 4. Thus, the spacer sheets 5 are preferably made of a soft material having surface resistivity of $10^6 \Omega/\square$ or less. The spacer sheets 5 used in the present embodiment were made of polyolefin synthetic paper having bending resistance of 80 mm or less and formed into a size of 230 μm in thickness and 300 mm in diameter. Dust repellency of the spacer sheets 5 should be low enough to keep the wafers clean. The dust repellency (Japan ICI Company Standard) of the spacer sheets 5 used herein was 200 particles ($>0.5 \mu\text{m}$) / 100 mm x 100 mm or less.

On the other hand, end-cushioning materials 6A and 6B are put to the uppermost portion and the bottom portion in the containing space 2e of the container main body 2. The wafers 4 and the spacer sheets 5 are stacked one after the other, while the end-cushioning materials 6A and 6B are put to both end portions of thus stacked contents to keep them between the materials. Owing to these end-cushioning materials 6A and 6B, the stacked wafers 4 are not directly in contact with the bottom base 2 of the container main body 2 and the ceiling ribs 3c of the lid body 3. The end-cushioning materials 6A and 6B are moderately impact absorbable and can absorb impact when the wafer container 1 is impacted, no impact being transmitted to the wafers.

The end-cushioning materials 6A and 6B used in the present embodiment was prepared from a soft polyurethane foam sheet of 200 in diameter and 15 mm in thickness, properties thereof being surface resistivity of

$8 \times 10^{11} \Omega/\square$ or less, charge voltage decay time (50 %) of 1.4 sec or less, density (JIS K6401) of 27 kg/m^3 , hardness (JIS K6401) of 6.5 kgf and impact resilience (JIS K6402) of 40 %. The impact resilience of a soft polyurethane foam sheet as the end-cushioning material is preferably about 20 to 60 %.

As has been described, 25 wafers 4 are stacked through the spacer sheets 5 contained in the containing space 2e of the container main body 2 in a situation where the end-cushioning materials 6A and 6B are put to the upper and bottom end portions of the contents as shown in Fig. 4.

In the above mentioned situation, the lid body 3 is put and fixed on the container main body 2 (not shown), thereby the wafers 4 being contained in the container 1 and ready for transporting and storing. When the wafers 4 and the spacer sheets 5 are stacked with the end-cushioning materials 6A and 6B followed by fixing the lid body 3 thereon, the ribs 3c formed on the ceiling surface of the lid body 3 impress the end-cushioning material 6A.

In more concrete terms, when the lid body 3 is put on the container main body 2 at a inclined position, the ribs 3c are in contact with the end-cushioning material 6A at this position, while the lid body 3 is turned to securely engage the slant protrusions 2d of the container main body with the slant slots 3e. As the lid body 3 advances in the direction of the stacked layer, the ribs 3c gradually impress the end-cushioning materials 6A and 6B downward (see, a blank arrow shown in Fig. 5). When the lid body 3 is completely fixed on the container main body 2, each of the upper and the bottom end-cushioning materials 6A and 6B lose about 10 % of thickness, thereby resulting in stress. The stress functions to hold the stacked wafers 4 so that the wafers 4, the spacer sheets 5 and the end-cushioning materials 6A and 6B are kept stably in the wafer container 1.

As the end-cushioning material 6A is slidably in contact with the ribs 3c by turning operation of the lid body 3, it is preferable for the end-cushioning material 6A to make a surface S thereof smooth so as not to turn around itself.

There may be used a spacer sheet structure which does not cause adhesion with the wafers 4 essentially or which breaks of such an adhesion easily if occurred, as shown in Figs. 6(A) and (B).

Fig. 6 (A) shows a spacer sheet of the above mentioned type having embosses 51 on the surface. The embosses 51 may be formed not only on the above mentioned spacer sheet but on a non-conductive spacer sheet. Further, if a structure thereof does not cause adhesion with the wafers 4 essentially or breaks of such an adhesion easily if occurred, there may be applied any pattern of the embosses 51 on the spacer sheet, such as a scattering pattern as shown in Fig. 6 (A), radiating one extending from the center of the sheet to outside, or stripes or lattice-like one.

Apparent thickness of an embossed sheet is preferably about 1.1 to 3 times compared with that of the

sheet before embossing, depending on a specific embossing pattern or a sheet material to be used. When embossed spacer sheets are used, a buffering effect thereof to the wafers can be increased.

Using synthetic paper spacer sheets 5 of the above mentioned type, wafers 4 were contained in the wafer container 1, which was then vacuum-packaged as a whole. After the package was opened, a test for putting out the wafers 4 from the container 1 was carried out. As a result, it was found that the wafers 4 did not adhere with the spacer sheets 5 and were put out from the container without trouble. Further, using embossed spacer sheets of 120 μm in thickness prepared from a polyethylene sheet of about 50 μm in thickness containing 30 % by weight of carbonblack, a similar test was repeated. Here again, the wafers 4 did not adhere with the spacer sheets thus prepared.

Fig. 6(B) shows a spacer sheet 5 of the above mentioned type in which there are formed a plurality of cutlines 52 extending from the periphery to inside. Similarly as in the case of the embossed sheet shown in Fig. 6(A), the cutlines 52 may be formed not only on the spacer sheet 5 of the above mentioned type but applicable to any other sheet. Further, if a structure thereof does not cause adhesion with the wafers 4 essentially or breaks of such an adhesion easily if occurred, there may be applied any pattern of the cutlines 52 on the spacer sheet, such as the pattern extending from the periphery to the center as shown in Fig. 6(B) or a similarly extending scroll.

Preferably, numbers of the cutlines 52 is 2 to 36, depending on a material of the spacer sheet.

A method for putting wafers in and/or out of the container of the present invention will be described in an embodiment in which the present method is applied to a wafer carrier system. Fig. 7 is a schematic perspective view of a wafer putting in and/or out system. Figs. 8, 9 and 10 are schematic views showing procedures for containing wafers in order in a container by means of the system.

In Fig. 7, a plurality of wafers 4 are carried on a conveyor 7 and led to a containing process successively. On the other hand, a container main body 2 for containing the wafers 4 is also led to the containing process by means of the other conveyor 8 and waits for the wafers 4 to be contained in order. A carrier robot 9 is arranged between both conveyers 7 and 8, a pickup arm 9a of the robot 9 being provided with a vacuum absorbing portion 9b on an end thereof. The wafers 4 carried on the conveyor 7 are absorbed in vacuo by means of the vacuum absorbing portion 9b and then contained in the container main body 2. While, another carrier robot 10 is similarly constructed as the robot 9 and carries and puts spacer sheets 5 and end-cushioning materials 6A and 6B on a conveyor (not shown) in the container main body 2.

In the above mentioned putting in and/or out system, first of all, an end-cushioning material 6B is put in a vacant container main body 2 carried on the conveyor

8 by means of the carrier robot 10 or by hand. The spacer sheet 5 is absorbed by the vacuum absorbing portion 10b of the carrier robot 10 to put in a containing space 2e of the container main body 2 from upside thereof. In this instance, a pickup arm 10a of the carrier robot 10 can put the absorbed spacer sheet 5 into the bottom of the containing space 2e through a slit (designated by 2c1 in Fig. 7).

An absorbed condition in the vacuum absorbing portion of the pickup arm 10a of the carrier robot 10 is canceled to release the spacer sheet 5 on the bottom of the containing space and to put it on the end-cushioning material 6B, the pickup arm 10a then being sent back to the initial position.

The carrier robot 9 is then operated to absorb the wafer 4 on the conveyor 7 (see, pickup arm 9a drawn by a broken line in Fig. 8), send it up to the container main body 2 (see, pickup arm 9a drawn by a solid line in Fig. 8), drop down the end of the pickup arm 9a in the containing space 2e and release the wafer 4 from an absorbed condition at a position close to the stacked spacer sheet (or a surface contact position with the spacer sheet) (see, Fig. 9). In this situation, the pickup arm 9a of the carrier robot 9 can drop down through a slit (designated by 2c3 in Fig. 7) other than the slit 2c1 of the container main body 2. Such drop down of the pickup arms 9 in the containing space 2e may be controlled by means of a distance sensor such as an optical sensor set on the vacuum absorbing portion 9b from distance measurement to the spacer sheet 5 so as to stop the end of the pickup arm at a predetermined position. It is also possible to set a torque sensor on a joint portion of the pickup arm 9a for detecting torque when the wafer 4 is in contact with the spacer sheet 5 so as to stop the pickup arm 9a. The carrier robot 9 goes up in the containing space 2e after releasing the wafer 4 (see, Fig. 10) and returns to the initial position to wait for the next carriage.

Another spacer sheet 5 is then stacked on the wafer 4 contained in the containing main body 2 by means of the carrier robot 10 similarly as described above, and in this manner, the spacer sheet 5 and the wafer 4 are successively stacked one after the other. When 25 wafers are stacked through the spacer sheet 5 in the container main body 2, the end-cushioning material 6A is finally put therein by means of the carrier robot 10 or by hand to complete a containing operation. After the containing operation is completed, the container main body 2 is carried on the conveyor 8, the lid body 3 is fixed thereto in the subsequent process to complete the whole containing process.

Fixing of the lid body 3 may be automatically carried out together with the wafer containing process or conducted by hand. Further, the wafers 4 and the spacer sheet 5 may either be handled by means of the same robot or put in the container by hand.

A process for putting out the wafers which have been contained in the wafer container is achieved by inversely conducting the above mentioned containing

process.

As is described above, the pickup arms 9a and 10a are controlled to stop the ends of both arms 9a and 10a at the uppermost position of the stacked wafer or the spacer sheet by detecting distance to the uppermost wafer or spacer sheet by means of a distance sensor. However, such a control system may also be conducted as in the following.

The container main body 2 is mounted on a table which is provided with a mechanism for moving vertically upward and downward depending on thickness of stacked wafers 4 or spacers 5 as they are put in or out. This makes it possible to always keep the uppermost level of stacked wafer 4 or spacer 5 at a constant level. Accordingly, the wafers 4 and the spacer sheets 5 can be put in and/or out of the container while keeping the levels of the pickup arms always at the same level.

In the above mentioned putting in and/or out system, positions of the container main body 2, the wafers 4 and the like are correctly controlled and carried by means of each conveyor and robot without any damage or breakage caused by accidental contact of the wafers with the cylindrical portion 2b when the wafers 4 are put in the container main body 2.

INDUSTRIAL APPLICABILITY

As slits are formed on a cylindrical portion of a container to enter therein a pickup arm of an automatic control mechanism, wafers can be easily put in the cylindrical portion of the container and put out therefrom. As a result, it is possible to prevent conventional damage or breakage caused by accidental contact of the wafers with the container when the wafers are put in the container by hand, thereby unexpectedly increasing the workability.

When spacer sheets put between each wafer is electrically conductive, static electricity is not generated if friction is occurred between the wafers and the spacer sheets due to mechanical vibration during transportation, or such static electricity does not remain in the container if generated. Accordingly, circuits formed on the wafers are not affected electrostatically.

When the spacer sheets have a structure which does not cause adhesion with the wafers 4 essentially or which breaks of such an adhesion easily if occurred, a put out operation of the wafers can be conducted smoothly without unification thereof with and the spacer sheets when the wafers are put out of the container.

Further, as the stacked wafers are contained in the container while being held between an elastic material, impact to the container due to dropping or vibration during transportation is absorbed by the elastic material and is not transmitted to the wafers, thereby preventing physical damage or breakage. Further, as a laminate consisting of the elastic material, the spacer sheets and the wafers is held vertically in the container, each wafer is kept stably in the container without resulting in positional deviation of mutual wafers, thereby controlling

breakage or damage due to accidental contact thereof with an inner wall of the container or generation of static electricity due to friction.

Claims

1. A semiconductor wafer container comprising a container main body provided with a cylindrical portion in which a plurality of semiconductor wafers and spacer sheets are stacked and contained, and a lid body to be put on the container main body.
2. A container claimed in claim 1 in which the cylindrical portion has at least one slit to receive a pickup arm for putting wafers in and/or out thereof.
3. A container claimed in claim 1 or 2 in which the container main body comprises the cylindrical portion standing on an ichnographically square base portion, and the lid body is ichnographically square form which is practically similar to ichnographically form of the base portion, the lid body and the container main body being fitted by relatively turning each other to ichnographically conform both bodies.
4. A semiconductor wafer container claimed in claim 1, 2 or 3 in which surface resistivity thereof is $10^6 \Omega/\square$ or less.
5. A containing structure of semiconductor wafers in which a plurality of semiconductor wafers are contained in a container main body through spacer sheets.
6. A containing structure of semiconductor wafers claimed in claim 5 in which the container main body is a container main body of the container claimed in claim 1, 2, 3 or 4.
7. A containing structure of semiconductor wafers claimed in claim 5 or 6 in which end-cushioning materials are put to upper and bottom end portions of a plurality of semiconductor wafers stacked and contained.
8. A containing structure of semiconductor wafers claimed in claim 5, 6 or 7 in which surface resistivity of the spacer sheet is $10^6 \Omega/\square$ or less.
9. A containing structure of semiconductor wafers claimed in claim 5, 6, 7 or 8 in which the spacer sheets are conductive synthetic paper of polypyrrole conjugated fiber.
10. A containing structure of semiconductor wafers claimed in claim 5, 6, 7, 8 or 9 in which the spacer sheets have a plurality of concave portions and/or

convex portions on at least one surface thereof.

11. A containing structure of semiconductor wafers claimed in claim 5, 6, 7, 8, 9 or 10 in which the spacer sheets have cutlines cut from periphery to inside thereof. 5
12. A method for putting wafers in and/or out of a container in which wafers and spacer sheets are put in and/or out of a cylindrical portion of a container main body by means of one pickup arm, the cylindrical portion having at least one slit thereon, characterized in that the pickup arm enters into at least one slit so as to put the semiconductor wafers and the spacer sheets in the cylindrical portion one after the other and put out of the cylindrical portion. 10 15
13. A method for putting wafers in and/or out of a container in which wafers and spacer sheets are put in and/or out of a cylindrical portion of a container main body by means of two pickup arms, the cylindrical portion having at least one slit thereon, characterized in that one of the pickup arm enters into the slit and the other one of the pickup arm enters into the same slit where said one of the pickup arm enters or a different slit so as to put the semiconductor wafers and the spacer sheets in the cylindrical portion one after the other and put out of the cylindrical portion. 20 25 30

Amended claims under Art. 19.1 PCT

- 1 A semiconductor wafer container comprising a container main body provided with a cylindrical portion in which a plurality semiconductor wafers and spacer sheets are stacked and contained, and a lid portion to be put on the container main body. 35
- 2 A container claimed in claim 1 in which the cylindrical portion has at least one slit to receive a pickup arm for putting wafers in and/or out thereof. 40
- 3 A container claimed in claim 1 or 2 in which the container main body comprises the cylindrical portion standing on an ichnographically square base portion, and the lid body is ichnographically square form which is practically similar to ichnographically form of the base portion, the lid body and the container main body being fitted by relatively turning each other to ichnographically conform both bodies. 45 50
- 4 A semiconductor wafer container claimed in claim 1, 2 or 3 in which surface resistivity thereof is $10^6 \Omega/\square$ or less. 55
- 5 A containing structure of semiconductor wafers in which a plurality of semiconductor wafers are contained in a container main body through spacer

sheets.

- 6 A containing structure of semiconductor wafers claimed in claim 5 in which the container main body is a container main body of the container claimed in claim 1, 2, 3 or 4.
- 7 A containing structure of semiconductor wafers claimed in claim 5 or 6 in which end-cushioning materials are put to upper and bottom end portions of a plurality of semiconductor wafers stacked and contained.
- 8 A containing structure of semiconductor wafers claimed in claim 5, 6 or 7 in which surface resistivity of the spacer sheet is $10^6 \Omega/\square$ or less.
- 9 A containing structure of semiconductor wafers claimed in claim 5, 6, 7 or 8 in which the spacer sheets are conductive synthetic paper of polypyrrole conjugated fiber.
- 10 A containing structure of semiconductor wafers claimed in claim 5, 6, 7, 8 or 9 in which the spacer sheets have a plurality of concave portions and/or convex portions on at least one surface thereof.
- 11 A containing structure of semiconductor wafers claimed in claim 5, 6, 7, 8, 9 or 10 in which the spacer sheets have cutlines cut from periphery to inside thereof.
- 12 A method for putting wafers in and/or out of a container in which wafers and spacer sheets are put in and/or out of a cylindrical portion of a container main body by means of one pickup arm, the cylindrical portion having at least one slit thereon, characterized in that the pickup arm enters into at least one slit so as to put the semiconductor wafers and the spacer sheets in the cylindrical portion one after the other and put out of the cylindrical portion.
- 13 A method for putting wafers in and/or out of a container in which wafers and spacer sheets are put in and/or out of a cylindrical portion of a container main body by means of two pickup arms, the cylindrical portion having at least one slit thereon, characterized in that one of the pickup arm enters into the slit and the other one of the pickup arm enters into the same slit where said one of the pickup arm enters or a different slit so as to put the semiconductor wafers and the spacer sheets in the cylindrical portion one after the other and put out of the cylindrical portion.
- 14(new) A semiconductor wafer container claimed in claim 1, 2, 3 or 4 in which surface resistivity of the spacer sheet is $10^6 \Omega/\square$ or less.

15(new) A semiconductor wafer container claimed in claim 1, 2,3,4 or 5 in which the spacer sheets are conductive synthetic paper of polypyrrole conjugated fiber.

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16(new) A semiconductor wafer container claimed in claim 1, 2,3,4,5 or 6 in which the spacer sheets have a plurality of concave portions

Statement under Art. 19.1 PCT

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Claim 14 makes clear that surface resistivity of the spacer sheet for a semiconductor wafer container claimed in claim 1, 2, 3 or 4 is $10^6 \Omega/\square$ or less.

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Claim 15 makes clear that the spacer sheets for a semiconductor wafer container claimed in claim 1, 2,3,4 or 5 are conductive synthetic paper of polypyrrole conjugated fiber.

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Claim 14 and 15 produce an effect which static electricity is not generated between the wafers.

Claim 16 makes clear that the spacer sheets for a semiconductor wafer container claimed in claim 1, 2,3,4,5 or 6 have a plurality of concave portions and/or convex portions on at least one surface thereof.

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Claim 16 produces an effect which no adhere tightly between the wafers and the spacer sheets.

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Fig.1

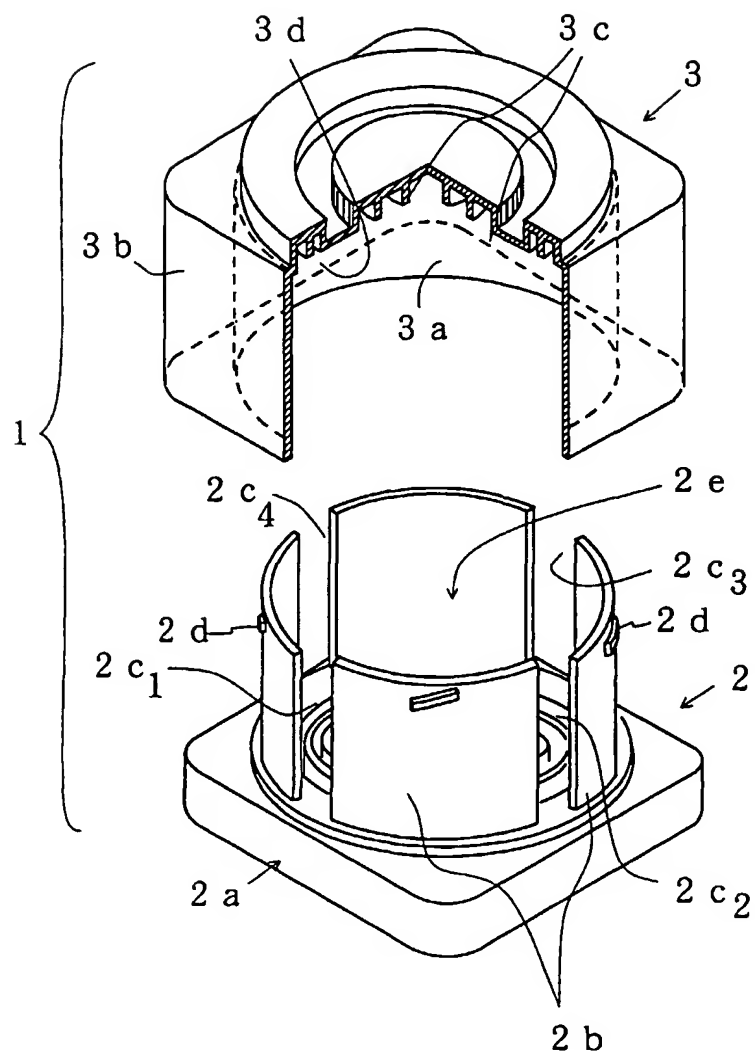


Fig.2

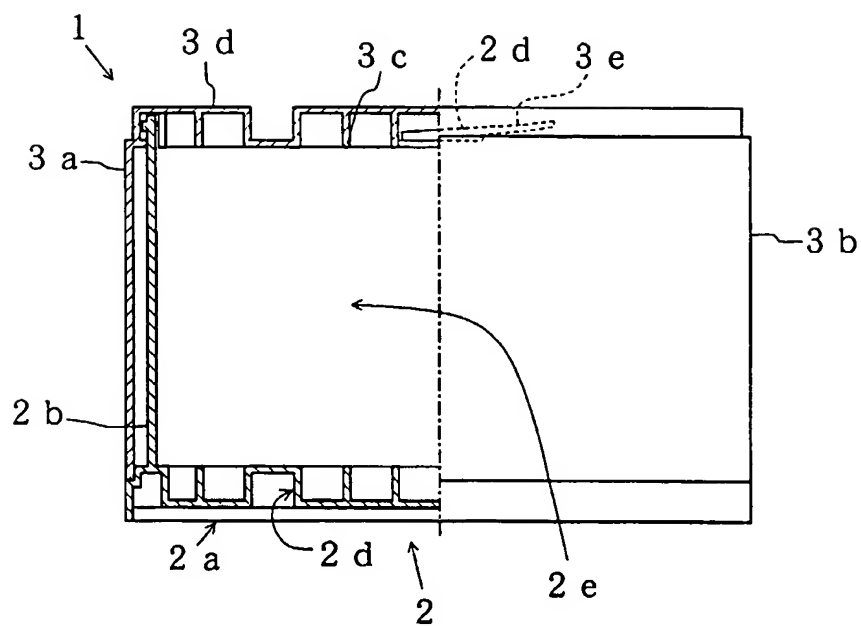


Fig.3

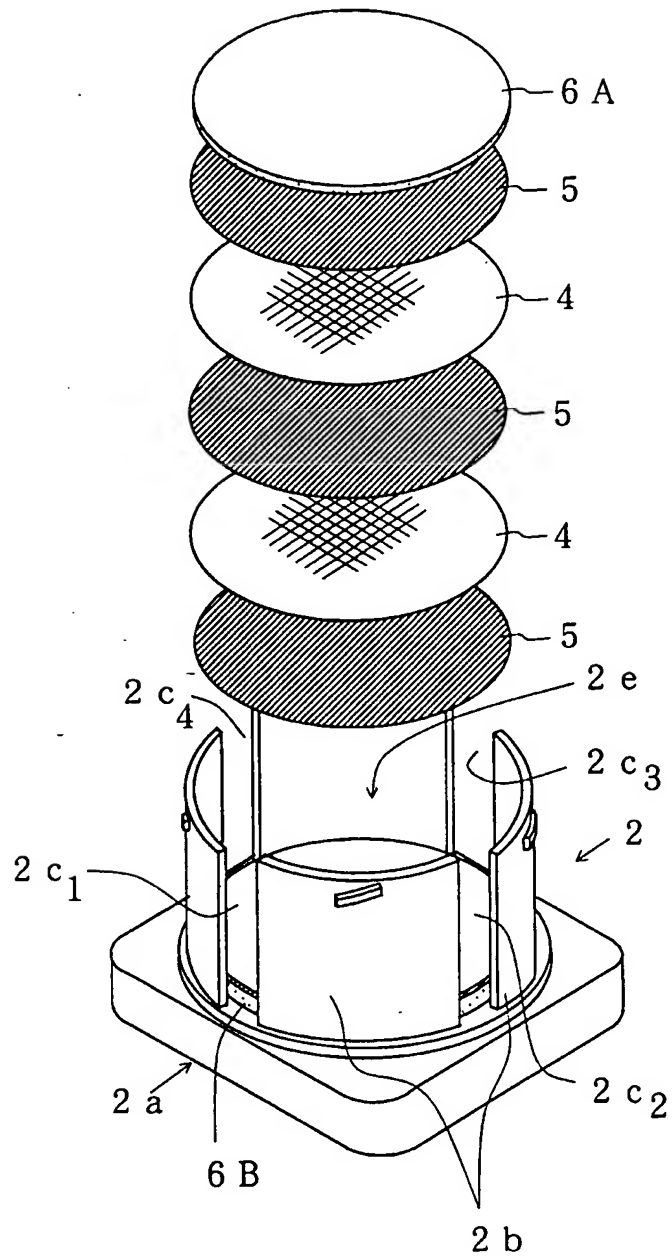


Fig.4

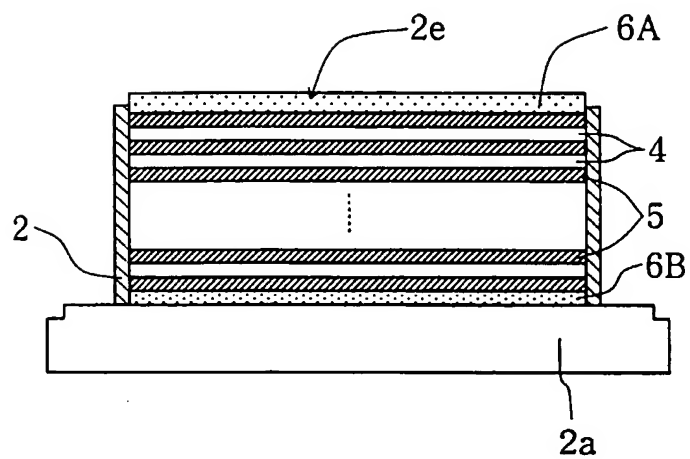


Fig.5

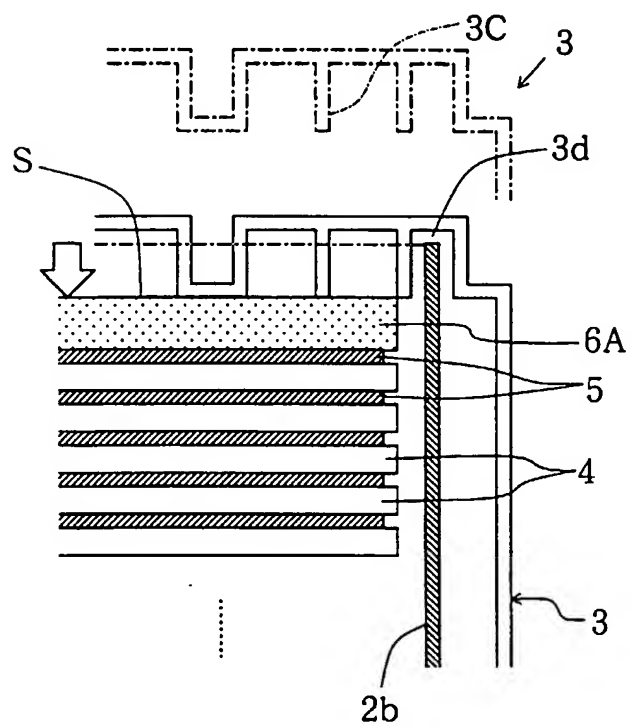
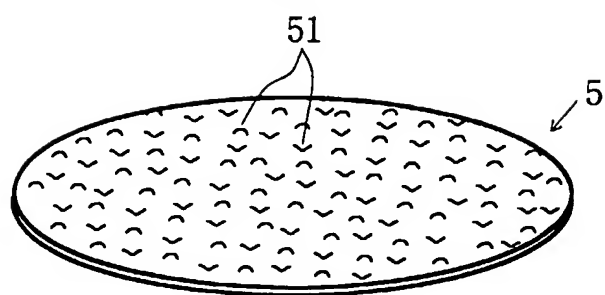
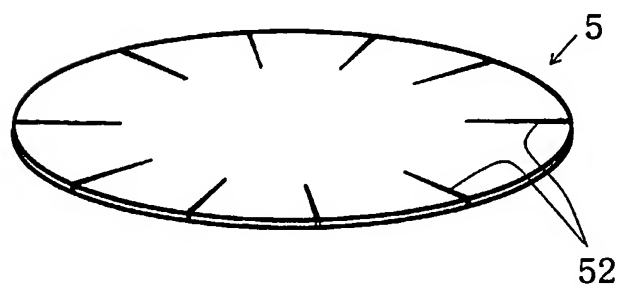


Fig.6



(A)



(B)

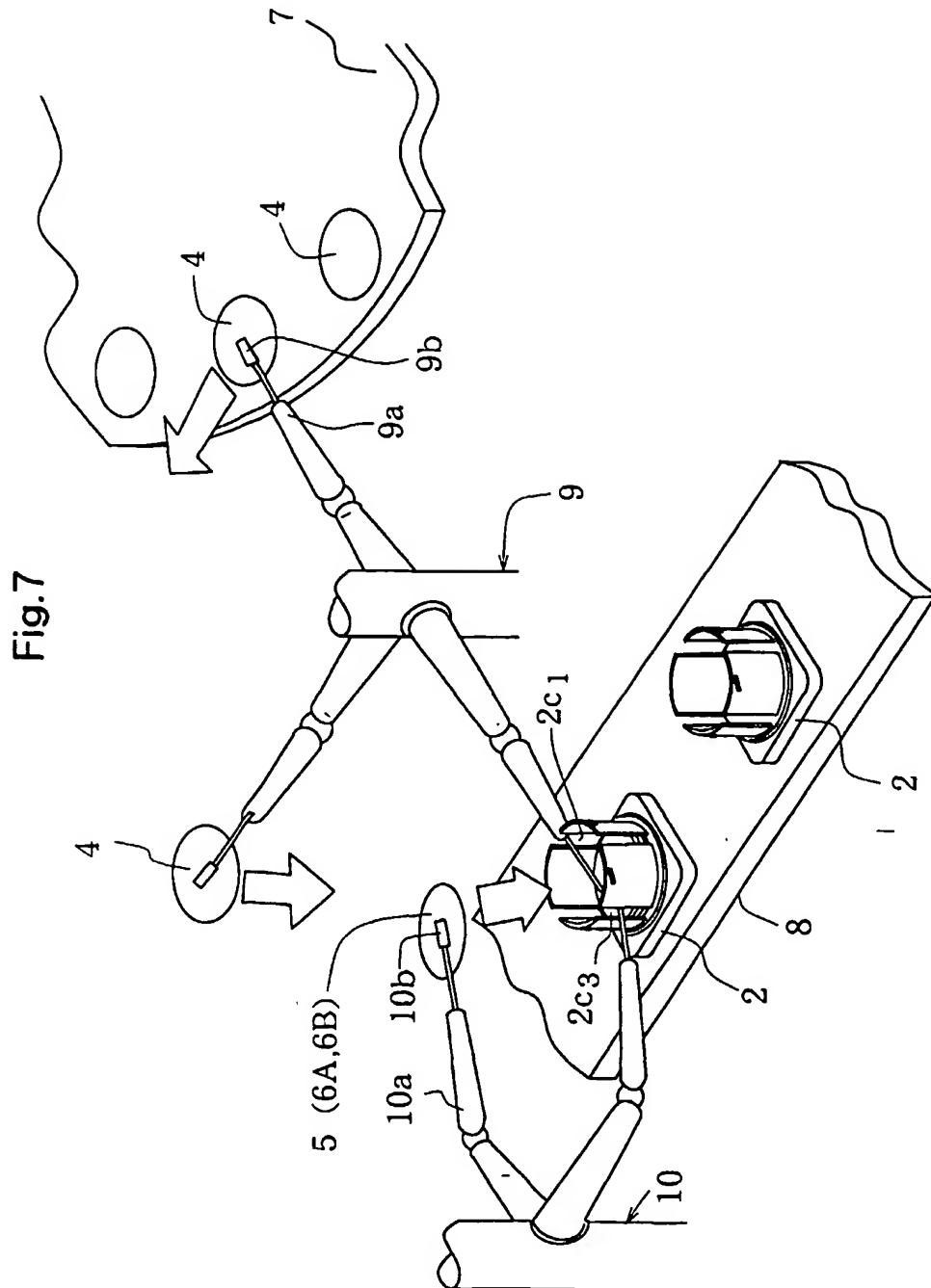


Fig.8

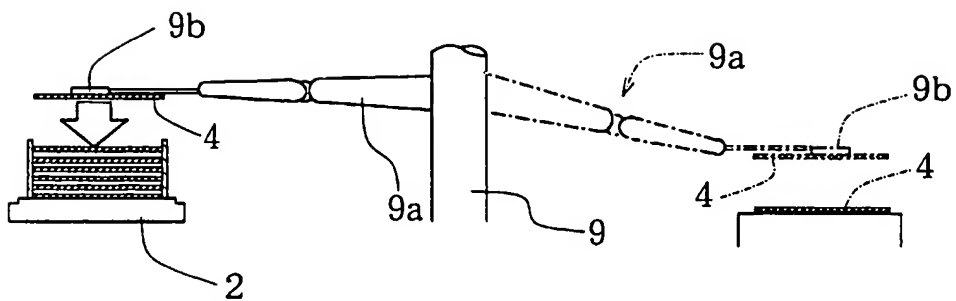


Fig.9

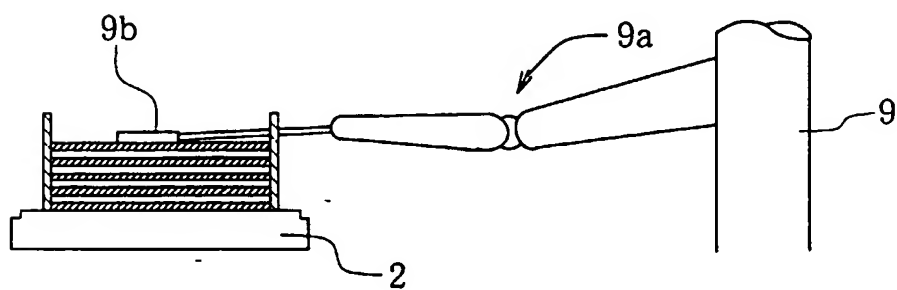
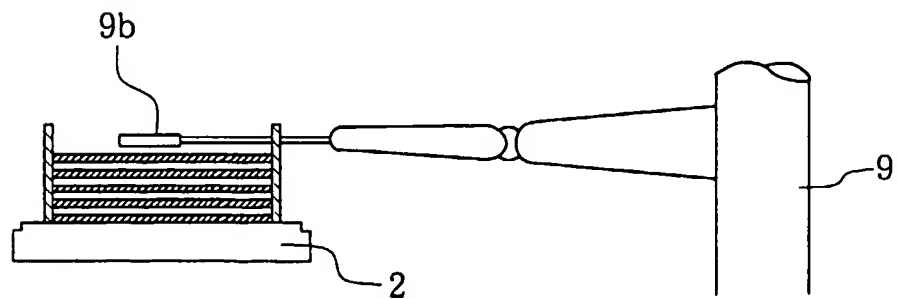


Fig.10



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP96/02441

A. CLASSIFICATION OF SUBJECT MATTER Int. Cl ⁶ H01L21/68, B65D85/38 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int. Cl ⁶ H01L21/68, B65D85/38 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926 - 1996 Kokai Jitsuyo Shinan Koho 1971 - 1996 Toroku Jitsuyo Shinan Koho 1994 - 1996 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 6-69328, A (Achilles Corp.), March 11, 1994 (11. 03. 94) (Family: none)	1 - 13
X	JP, 6-204315, A (Mitsubishi Materials Corp. and another),	5
Y	July 22, 1994 (22. 07. 94) (Family: none)	1-4, 6-13
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reasons (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search November 22, 1996 (22. 11. 96)		Date of mailing of the international search report December 3, 1996 (03. 12. 96)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

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